which case, the sequence described in [0047] works as well, even for initialization-less classes, although it is sub-optimal (for initialization-less classes, the sequence always branches to instruction 7, since the class is always initialized).

[0057] Lastly, the sequence of instructions described in [0054] to access a task's copy of a class's variables in cases when a class initialization barrier can be omitted for a class that is not initialization-less must be modified to support the arrangement of entries required when initialization-less classes are treated specially. The modification simply consists of changing Instruction 2 to add (TCM_POINTER_SIZE * NB_TASK_PER_TABLE) to the tcm_table register instead of TCM_POINTER_SIZE in order to compute the offset to the resolved entry (TCM_POINTER_SIZE is the size of a single task class mirror object reference, NB_TASK_PER_TABLE is number of tasks supported by the table).

[0058] The foregoing descriptions of embodiments of the present invention have been presented for purposes of illustration and description only. They are not intended to be exhaustive or to limit the present invention to the forms disclosed. Accordingly, many modifications and variations will be apparent to practitioners skilled in the art. Additionally, the above disclosure is not intended to limit the present invention. The scope of the present invention is defined by the appended claims.